Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

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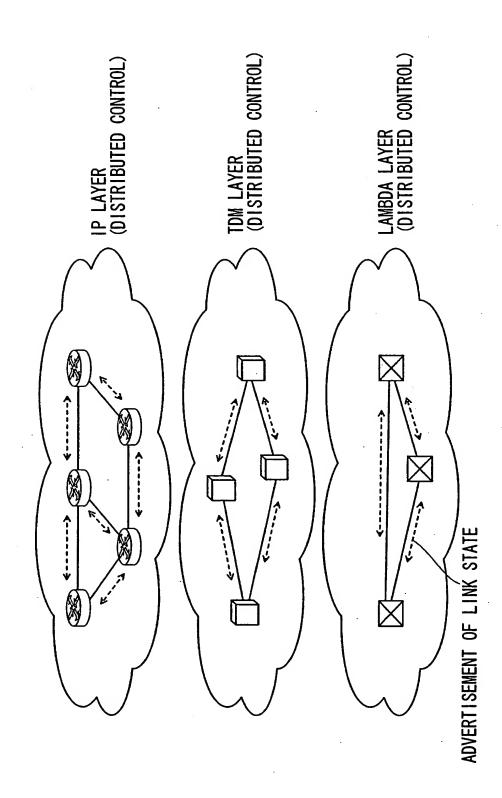
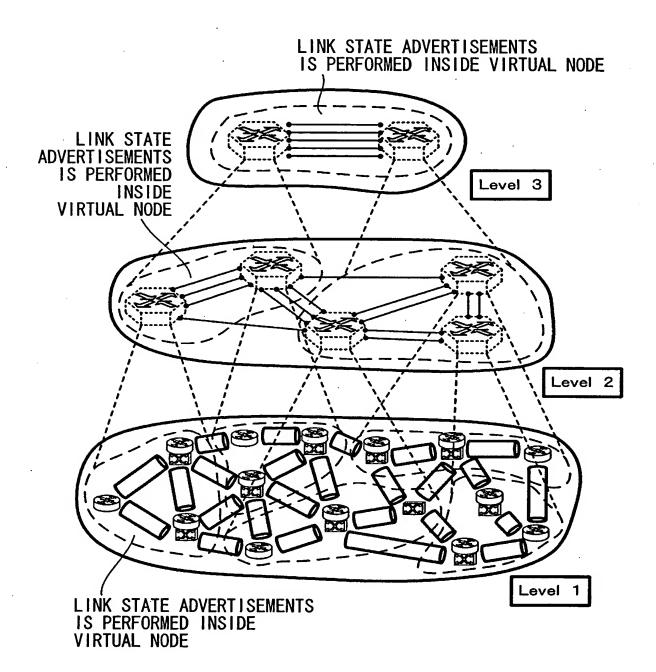


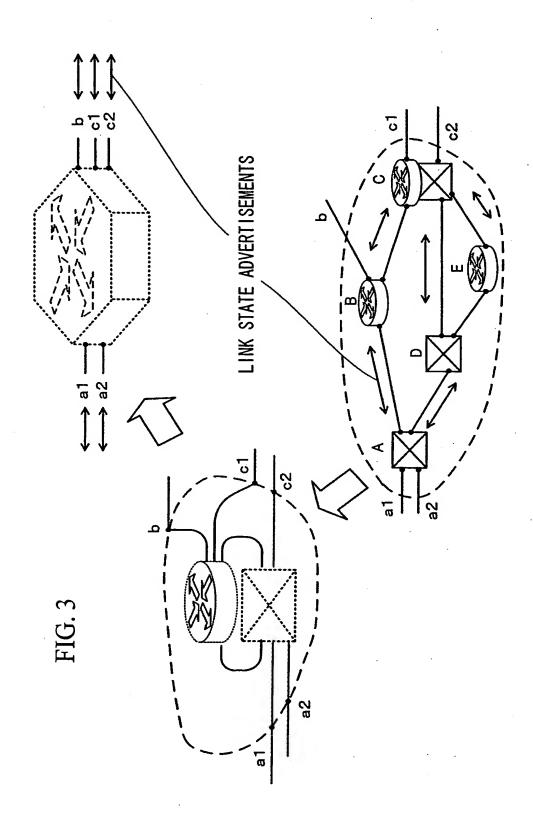
FIG.

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FIG. 2



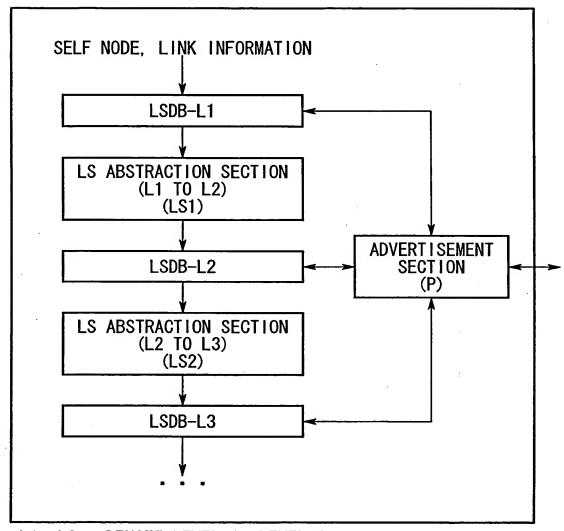
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Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

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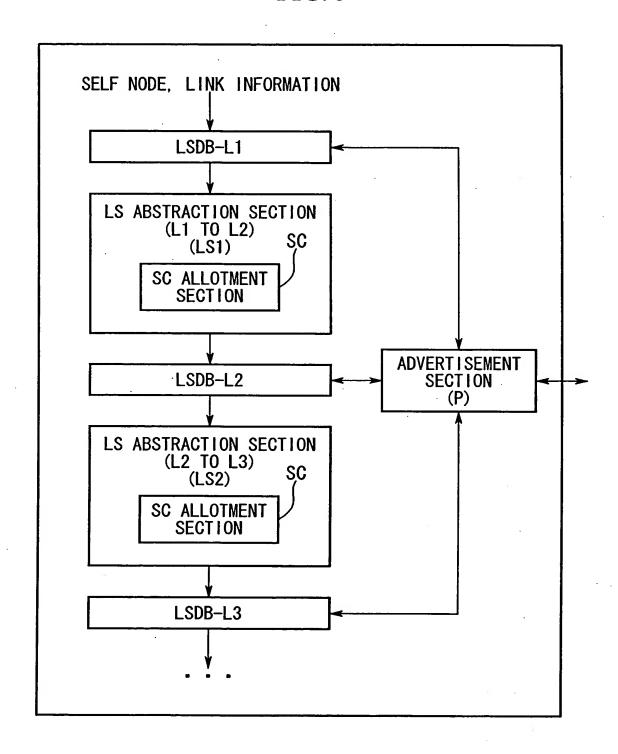
FIG. 4



L1, L2··· DENOTE LEVEL 1, LEVEL 2···

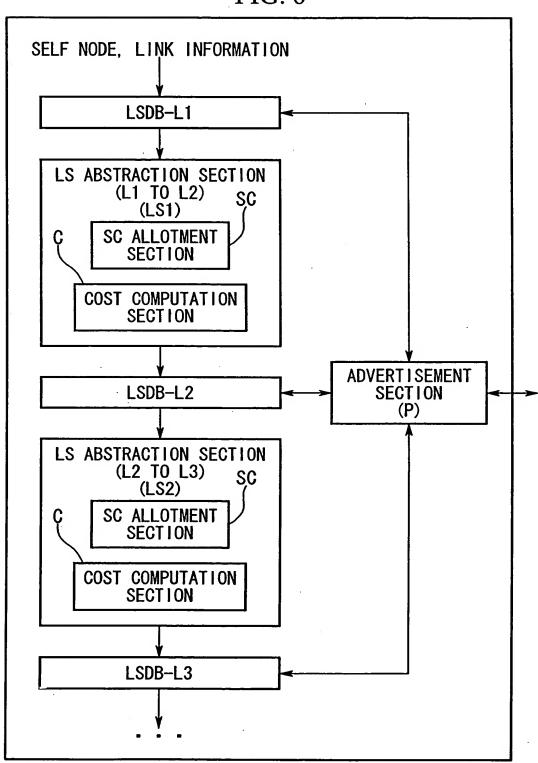
Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

FIG. 5



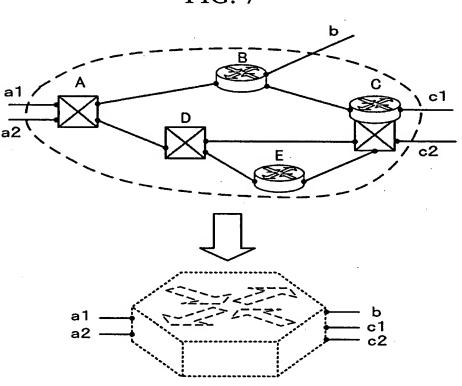
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FIG. 6



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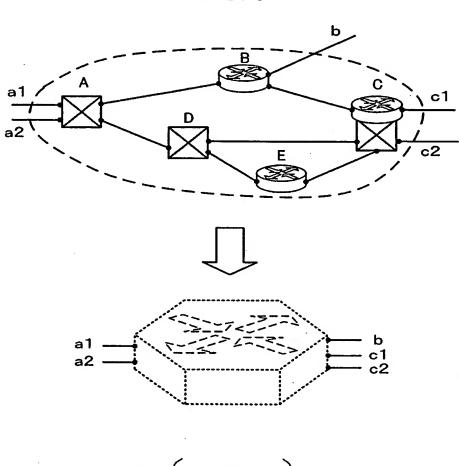
FIG. 7

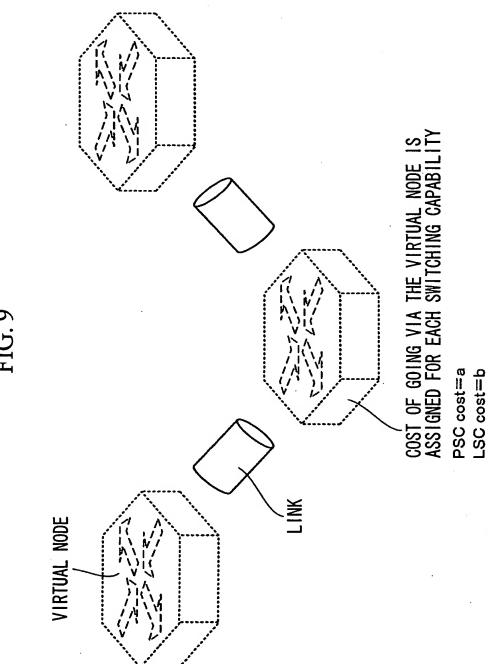


	a1	a2	b	c1	
a1	PSC, LSC	PSC, LSC	PSC	PSC	PSC, LSC
a2	PSC, LSC	PSC, LSC	PSC	PSC	PSC, LSC
b	PSC	PSC	PSC	PSC	PSC
c1	PSC	PSC	PSC	PSC	PSC
c2 .	PSC, LSC	PSC, LSC	PSC	PSC	PSC

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FIG. 8

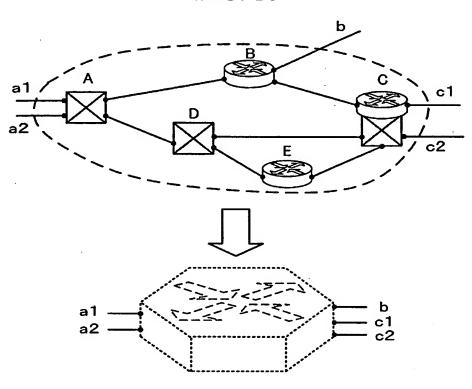




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FIG. 10

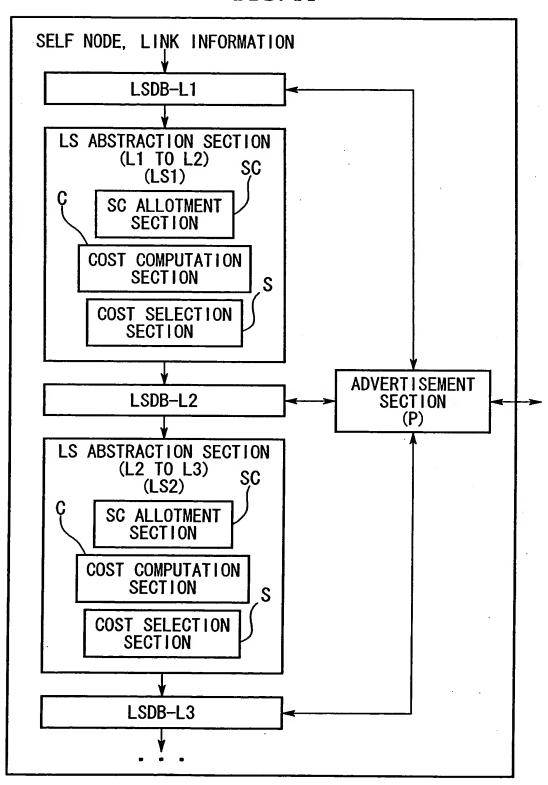


	a1	a2	b	c1	c2		
a1	PSC, LSC	PSC, LSC	PSC	PSC	PSC, LSC		
a2	PSC, LSC	PSC, LSC	PSC	PSC	PSC, LSC		
b	PSC	PSC	PSC	PSC	PSC		
c1	PSC	PSC	PSC	PSC	PSC		
c2	PSC, LSC	PSC, LSC	PSC	PSC	PSC		
OCCIO ALLOCATED INDIVIDUALLY							
	/ COSTS ALLOCATED INDIVIDUALLY						

AS THIS COST, THE COST IS ALLOTTED OF THE MINIMUM COST PATH FROM AMONG THE PATH CANDIDATES A-B-C, A-D-C, AND A-D-E-C

Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-00032

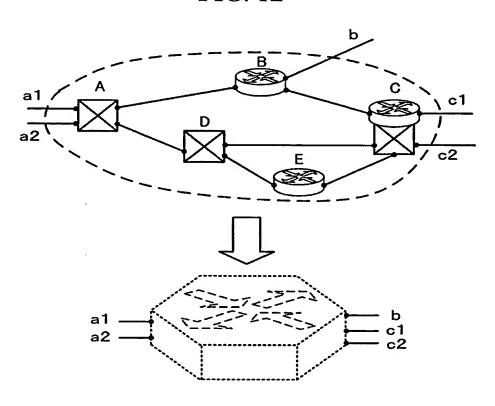
FIG. 11



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FIG. 12



	a1	a2	b	c1	c2		
a1	PSC, LSC	PSC, LSC	PSC	PSC	PSC, LSC		
a2	PSC, LSC	PSC, LSC	PSC	PSC	PSC, LSC		
b	PSC	PSC	PSC	PSC	PSC ·		
c1	PSC	PSC	PSC	PSC	PSC		
c2	PSC, LSC	PSC, LSC	PSC	PSC	PSC		
COSTS ALLOCATED INDIVIDUALLY							

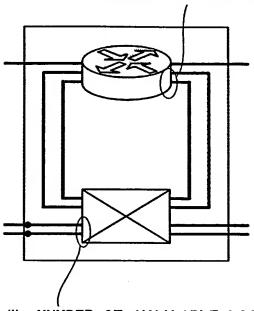
AS THIS COST, THE COST IS ALLOTTED OF THE MINIMUM COST PATH FROM AMONG THE PATH CANDIDATES A-B-C AND A-D-C

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FIG. 13

P: NUMBER OF AVAILABLE PSC INTERFACES

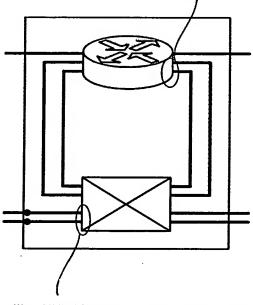


W: NUMBER OF AVAILABLE LSC INTERFACES

PSC cost=1/P LSC cost=1/W

FIG. 14

P: NUMBER OF AVAILABLE PSC INTERFACES PT: TOTAL NUMBER OF PSC INTERFACES



W: NUMBER OF LSC AVAILABLE INTERFACES

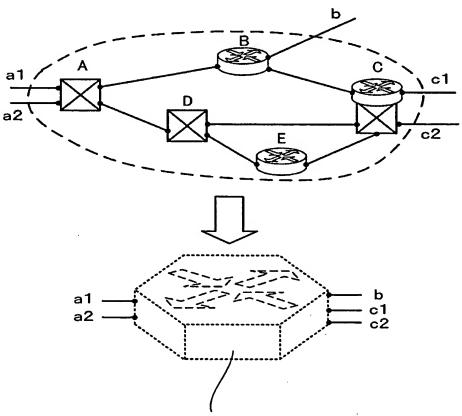
WT: TOTAL NUMBER OF LSC INTERFACES

PSC cost=1-P/PT LSC cost=1-W/WT

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FIG. 15



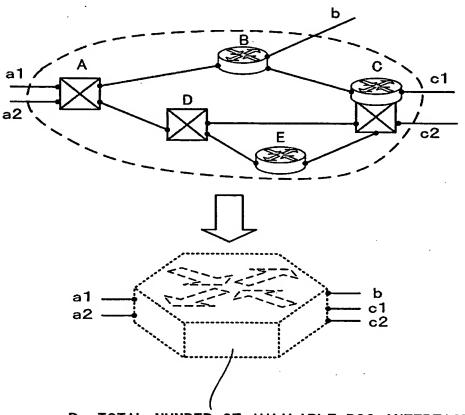
P: NUMBER OF AVAILABLE PSC INTERFACES W: NUMBER OF AVAILABLE LSC INTERFACES

PSC cost=1/P LSC cost=1/W

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FIG. 16



P: TOTAL NUMBER OF AVAILABLE PSC INTERFACES PT: TOTAL NUMBER OF PSC INTERFACES

W: TOTAL NUMBER OF AVAILABLE LSC INTERFACES

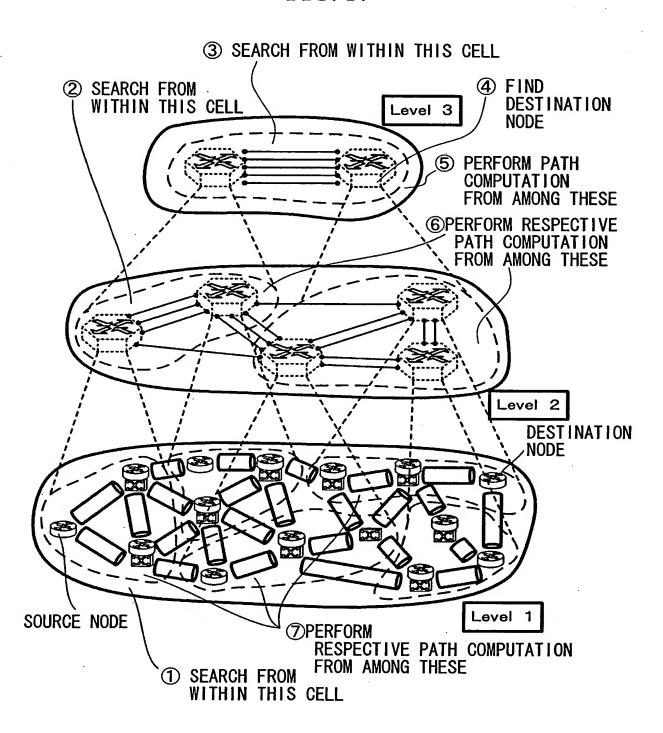
WT: TOTAL NUMBER OF LSC INTERFACES

PSC cost=1-P/PT LSC cost=1-W/WT

Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-00032

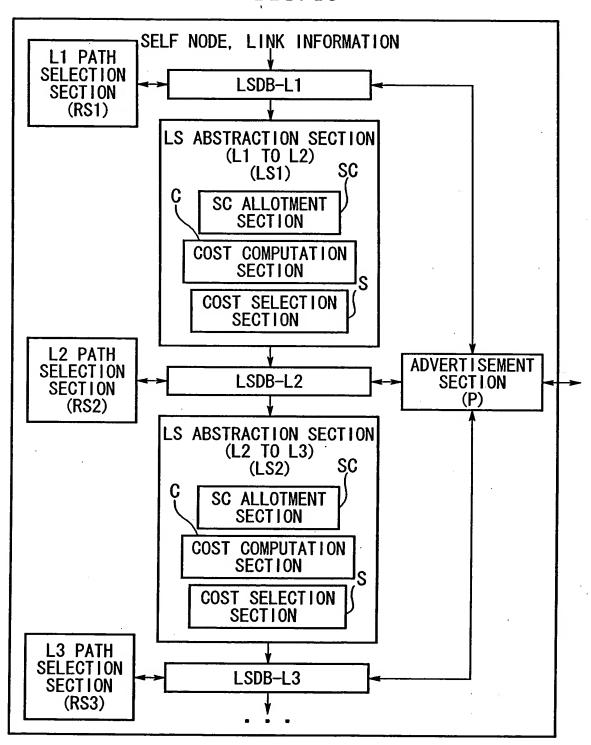
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FIG. 17



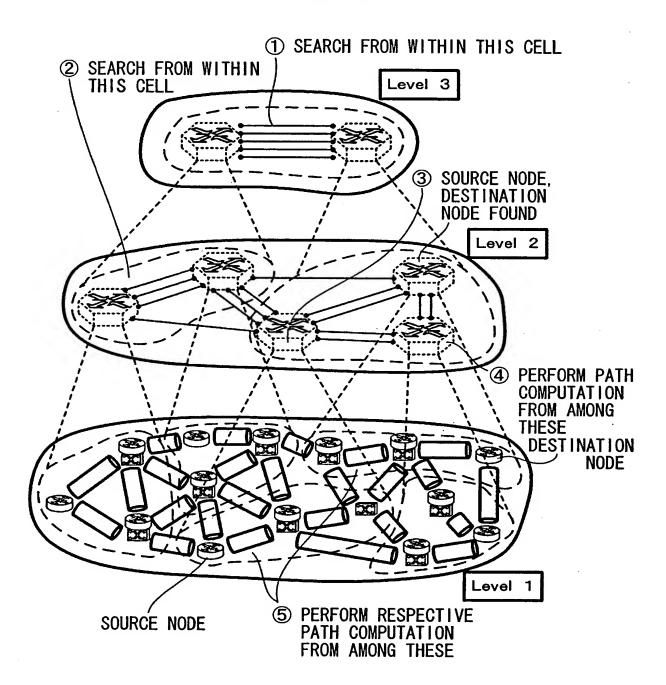
Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-00032

FIG. 18



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FIG. 19



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FIG. 20

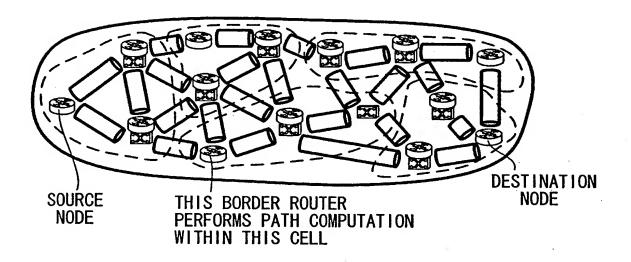
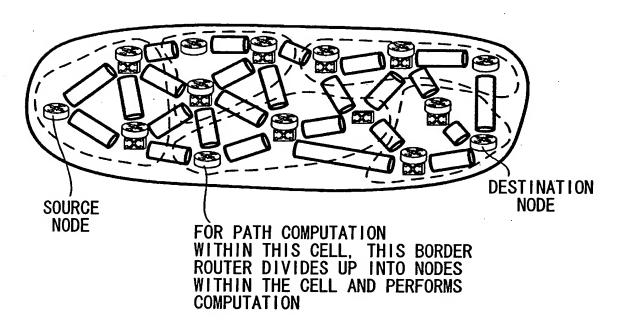
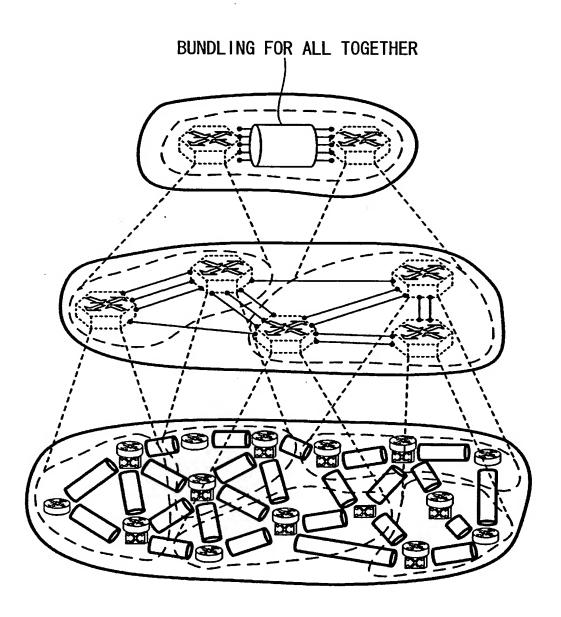


FIG. 21



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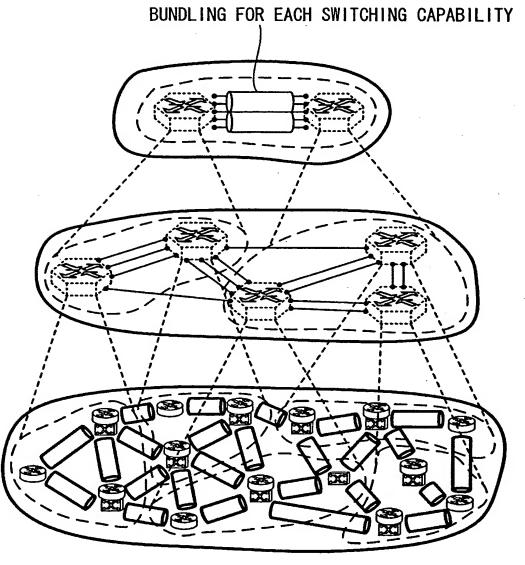
FIG. 22



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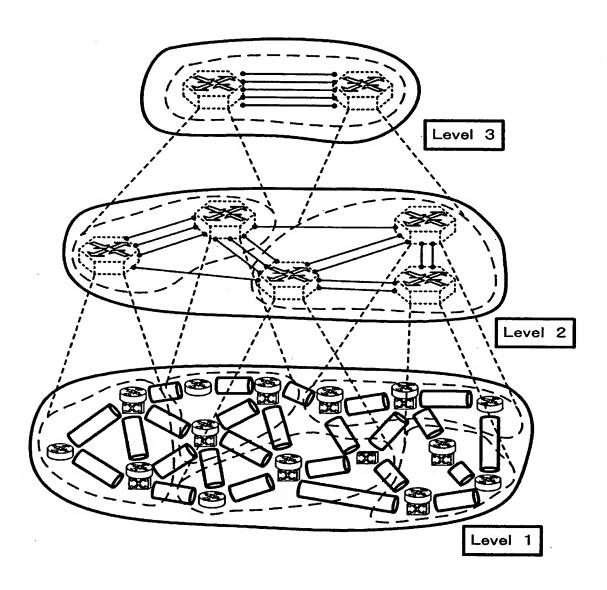
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FIG. 23



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Atty. Ref.: 5259-000032

FIG. 24

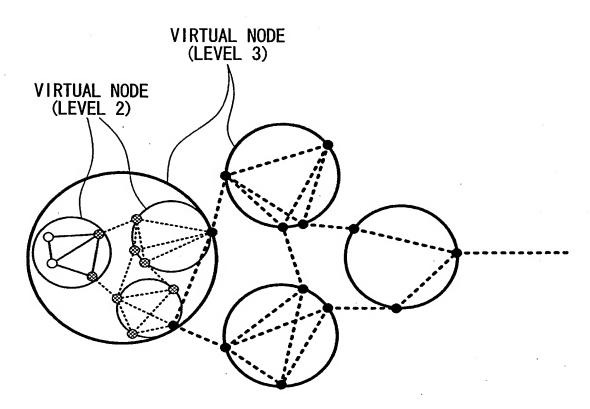


Title: LAYERED NETWORK NODE, NETWORK INCORPORATING

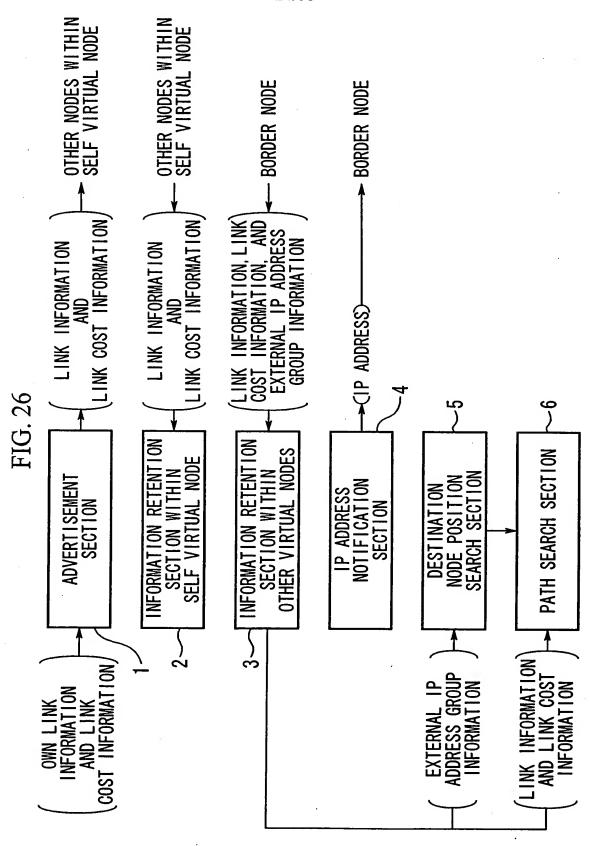
THE SAME, NODE, AND LAYERED NETWORK Inventor(s): Daisaku Shimazaki et al.
Atty. Ref.: 5259-000032

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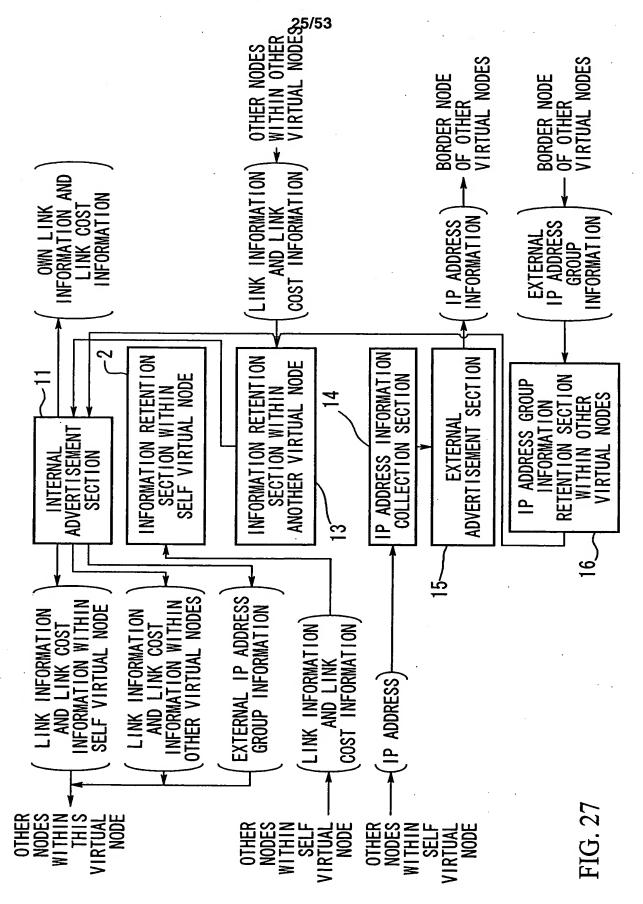
FIG. 25



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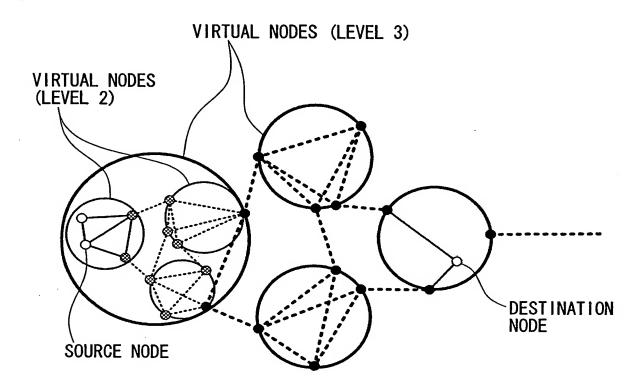
Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032



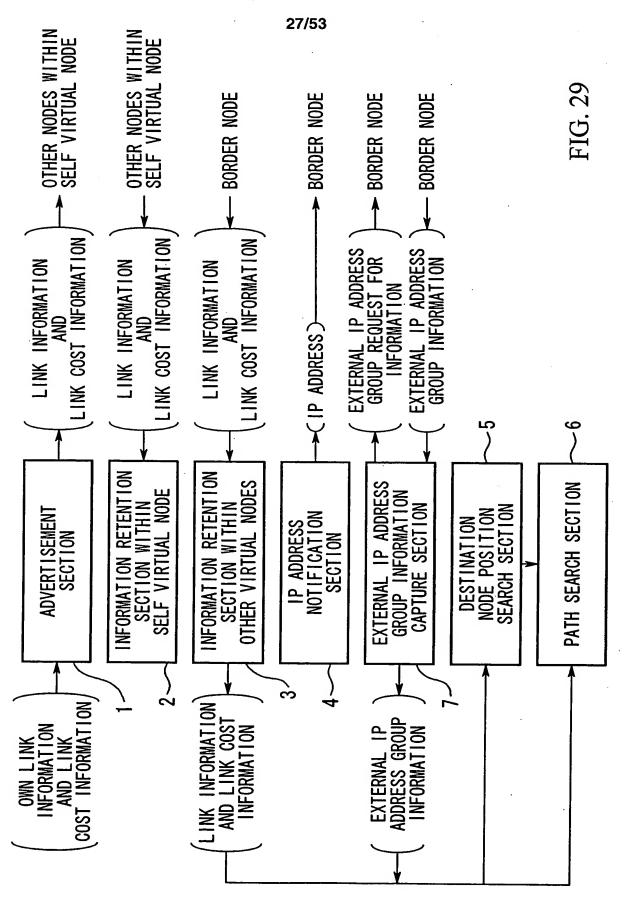
Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

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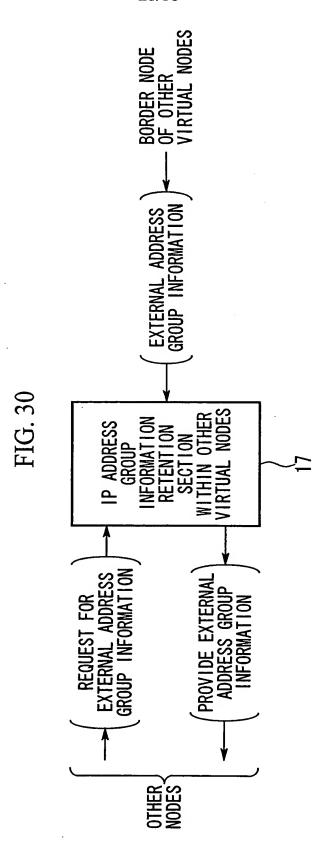
FIG. 28



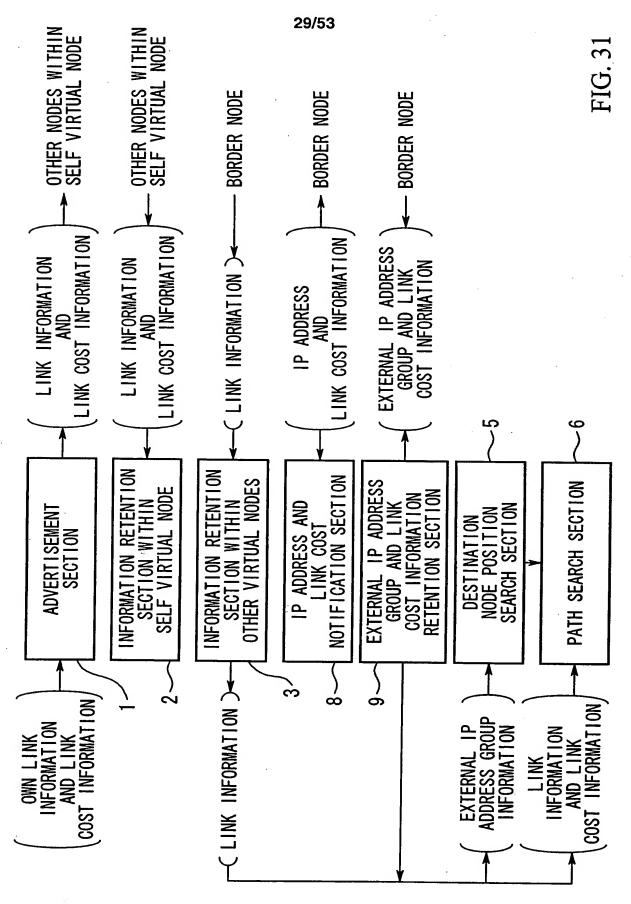
Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-00032



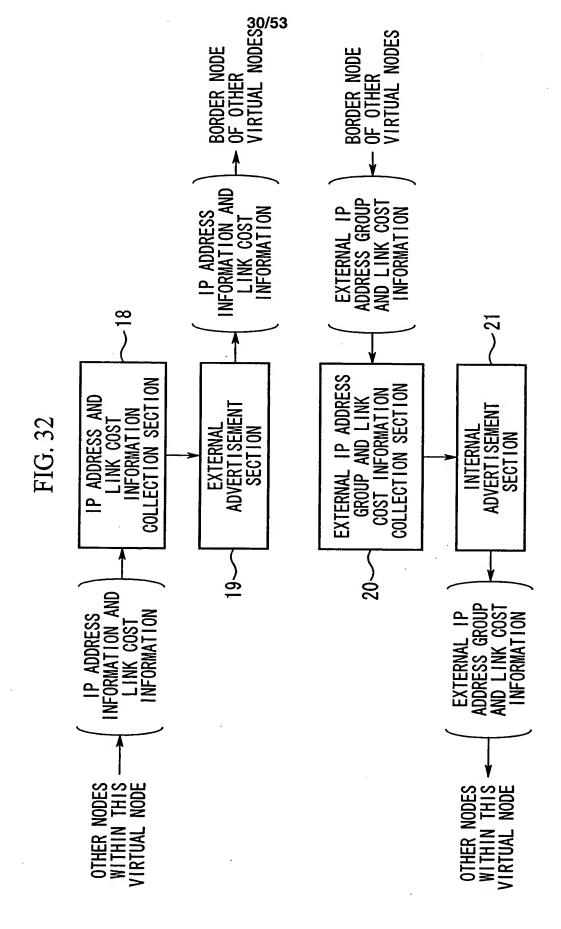
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Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

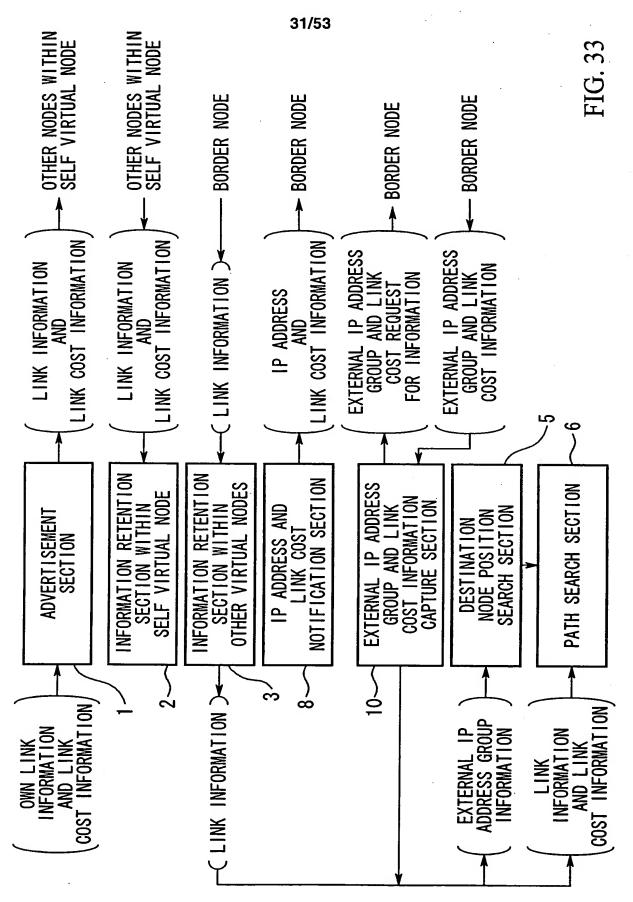


Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

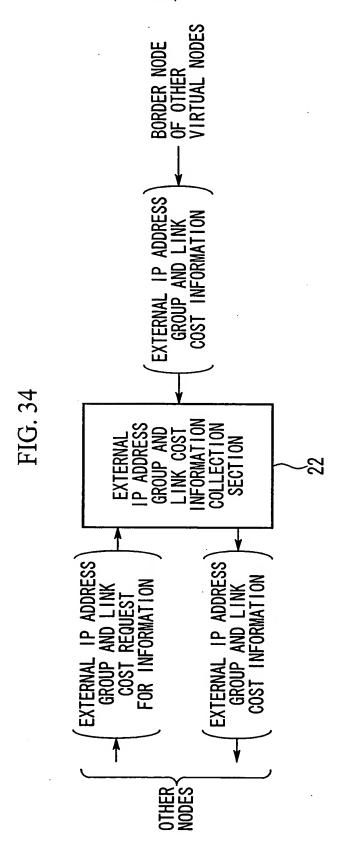


Title: LAYERED NETWORK NODE, NETWORK INCORPORATING THE SAME, NODE, AND LAYERED NETWORK Inventor(s): Daisaku Shimazaki et al.

Atty. Ref.: 5259-000032

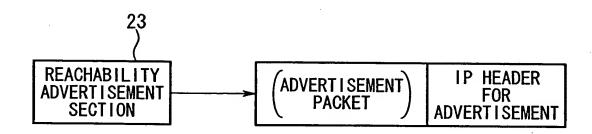


Title: LAYERED NETWORK NODE, NETWORK INCORPORATING THE SAME, NODE, AND LAYERED NETWORK Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

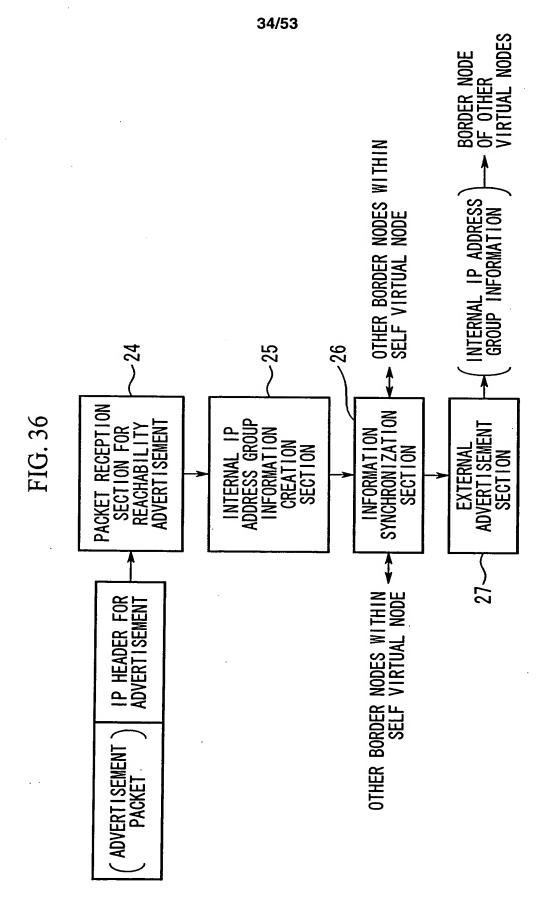


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FIG. 35



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Title: LAYERED NETWORK NODE, NETWORK INCORPORATING THE SAME, NODE, AND LAYERED NETWORK Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

FIG. 37

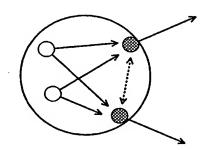


FIG. 38

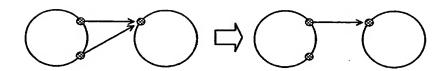
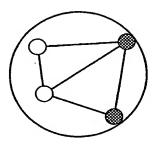
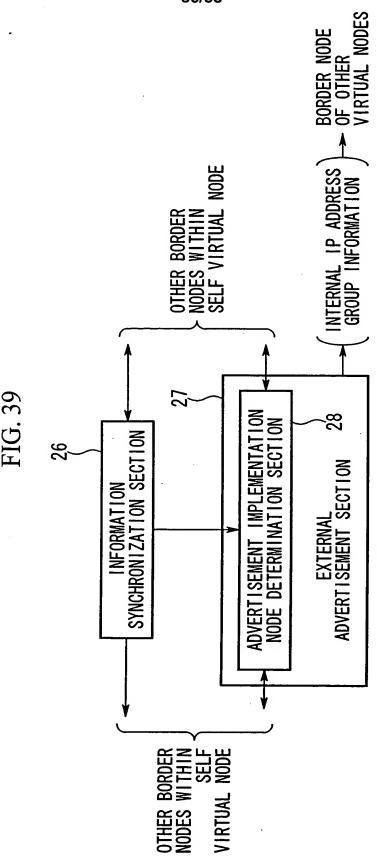


FIG. 44

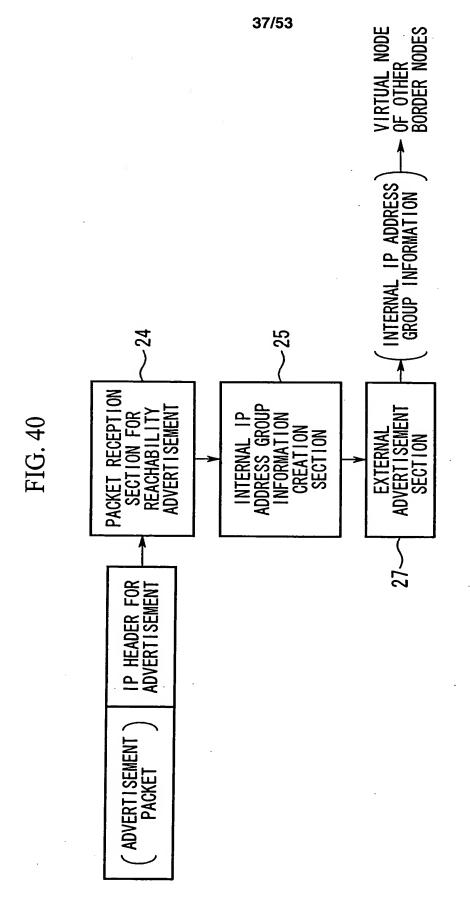


Title: LAYERED NETWORK NODE, NETWORK INCORPORATING THE SAME, NODE, AND LAYERED NETWORK Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032



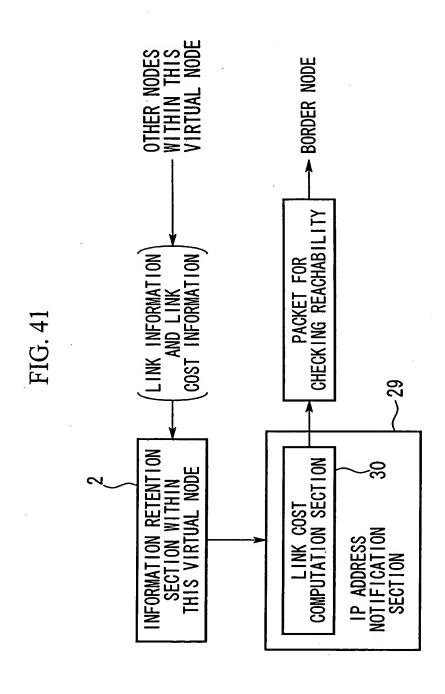


Title: LAYERED NETWORK NODE, NETWORK INCORPORATING THE SAME, NODE, AND LAYERED NETWORK Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032



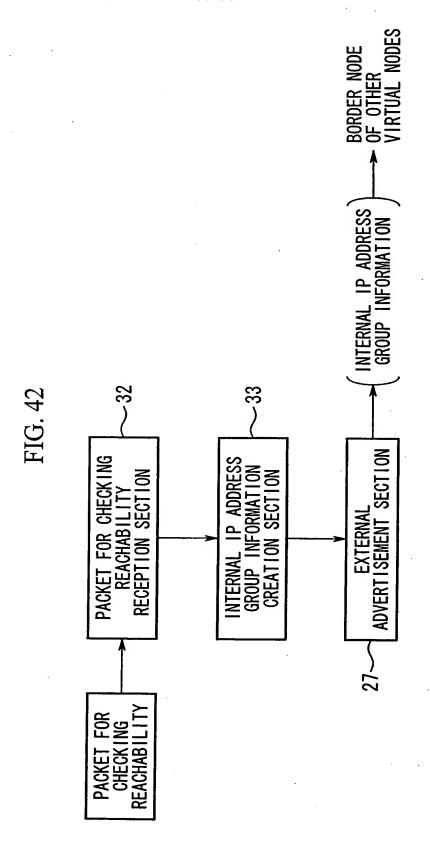
Title: LAYERED NETWORK NODE, NETWORK INCORPORATING
THE SAME, NODE, AND LAYERED NETWORK
Inventor(s): Daisaku Shimazaki et al.
Atty. Ref.: 5259-000032

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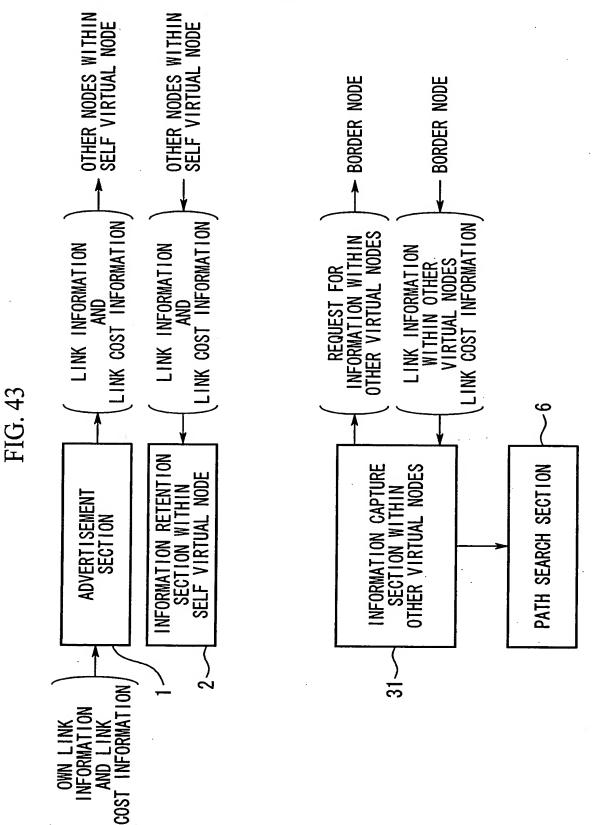


Title: LAYERED NETWORK NODE, NETWORK INCORPORATING THE SAME, NODE, AND LAYERED NETWORK Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

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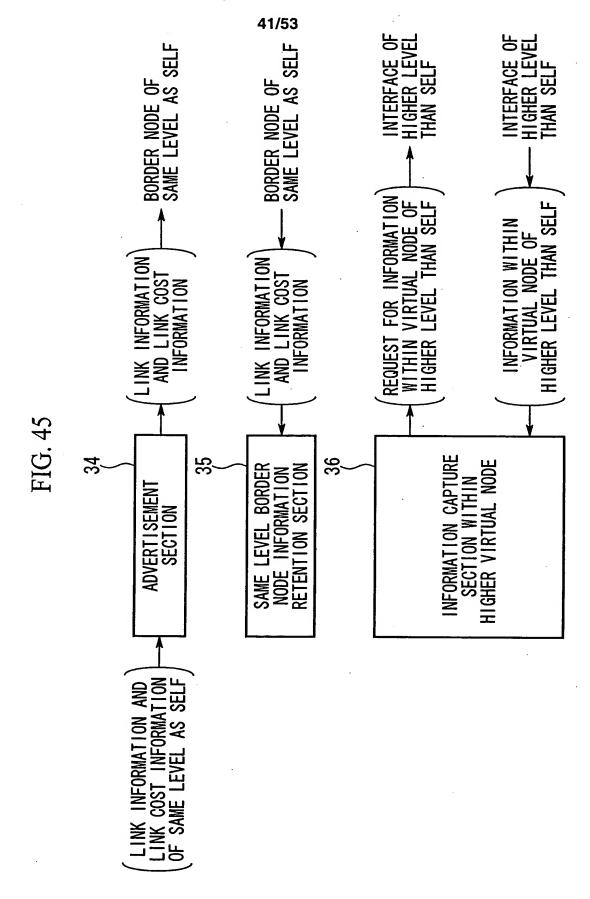


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Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032



Title: LAYERED NETWORK NODE, NETWORK INCORPORATING THE SAME, NODE, AND LAYERED NETWORK

Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

FIG. 46

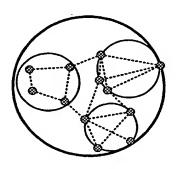
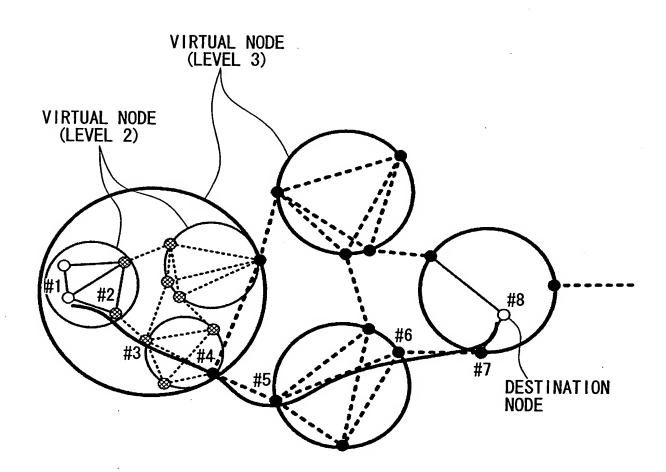


FIG. 47



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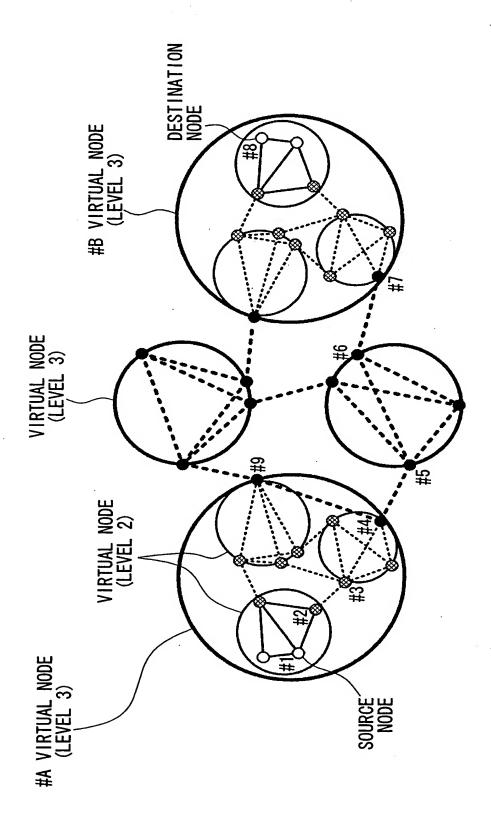
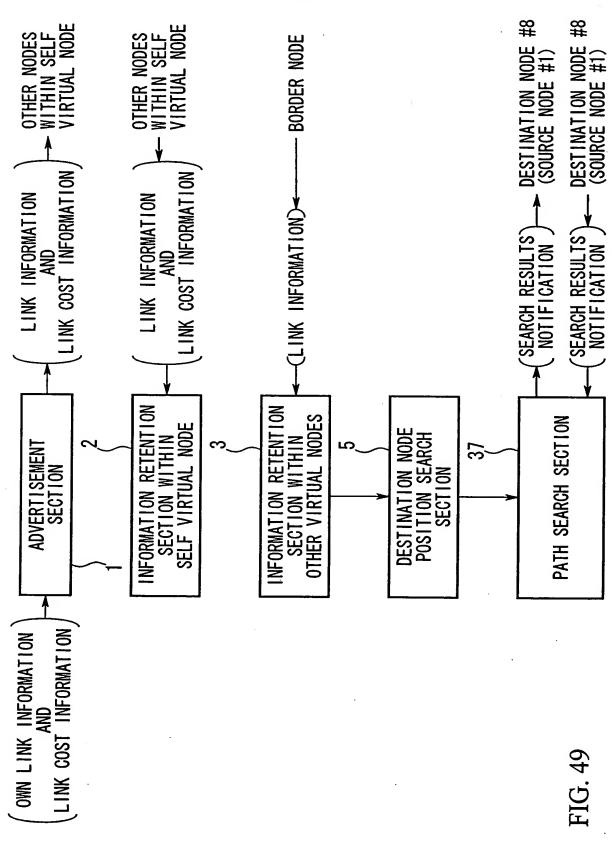


FIG. 48

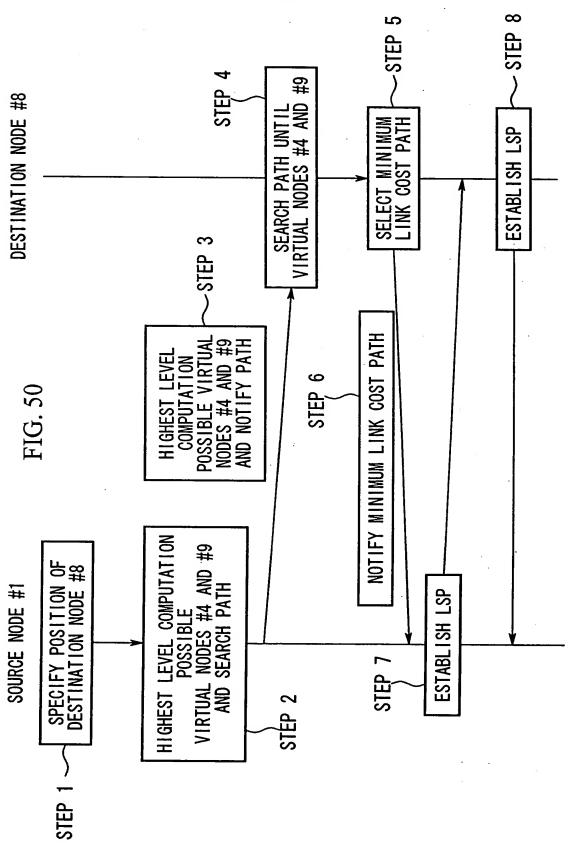
Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032





Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032





Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-00032

FIG. 51

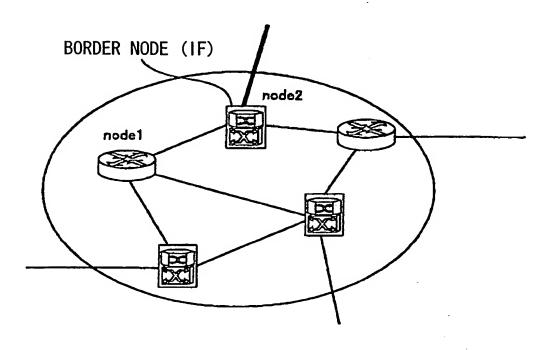
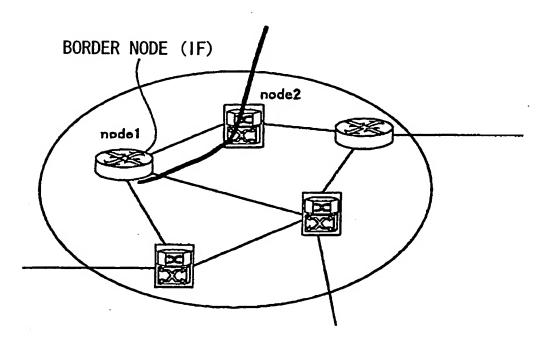
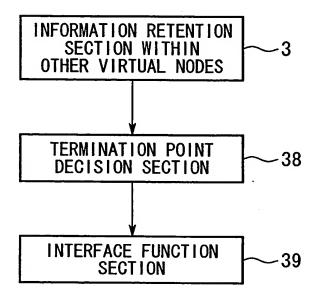


FIG. 52



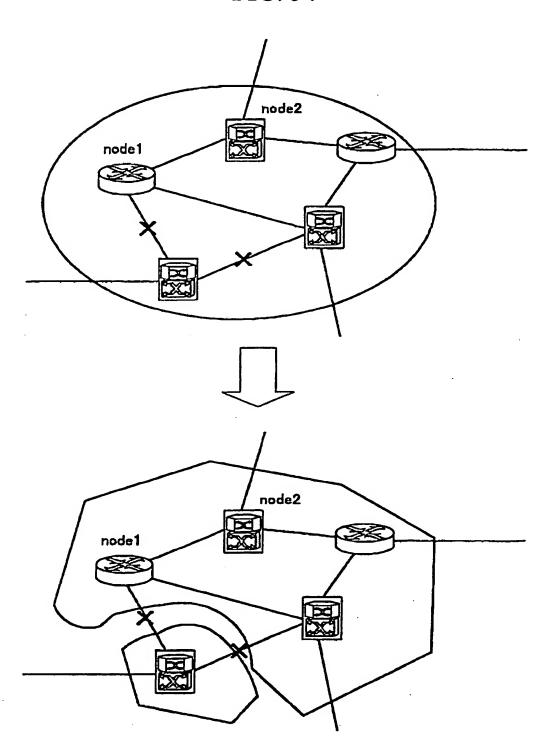
Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-00032

FIG. 53



Title: LAYERED NETWORK NODE, NETWORK INCORPORATING THE SAME, NODE, AND LAYERED NETWORK Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

FIG. 54



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FIG. 55

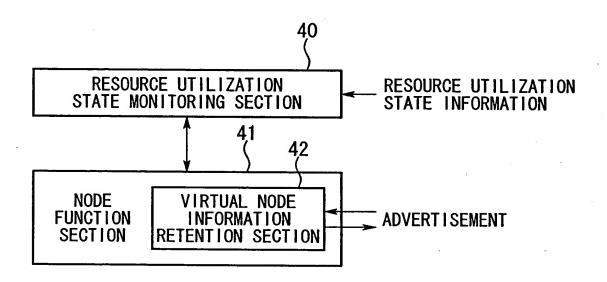
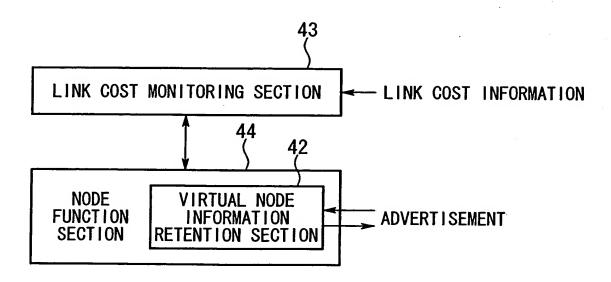


FIG. 56



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FIG. 57

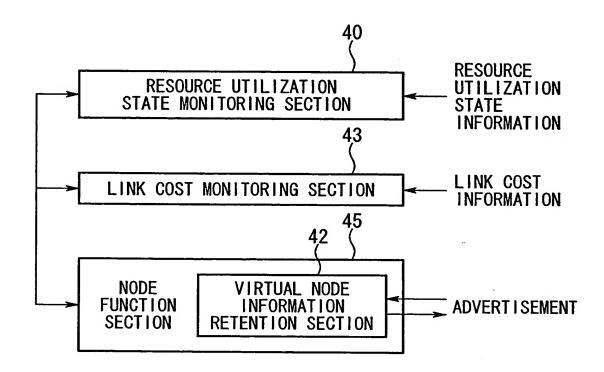
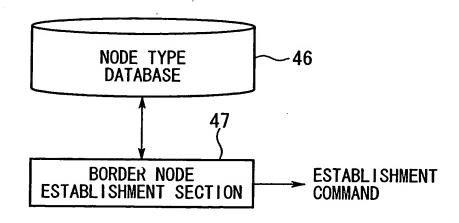


FIG. 58



Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-00032

FIG. 59

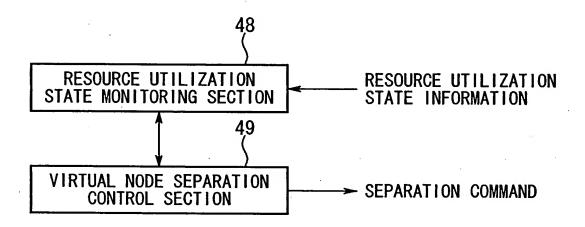
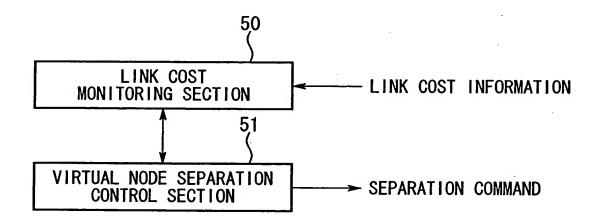
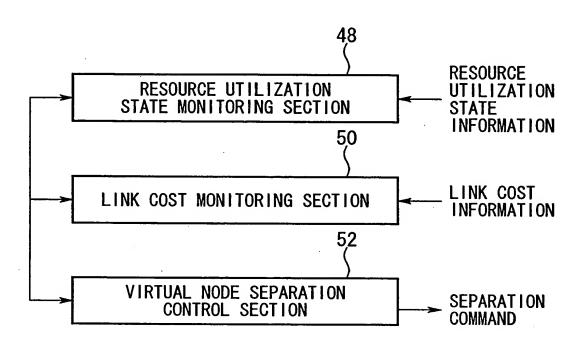


FIG. 60



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FIG. 61



Inventor(s): Daisaku Shimazaki et al. Atty. Ref.: 5259-000032

